# SWA Review Document

## Summary

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| --- | --- |
| **Date** | 22/01/2021 |
| **Effort** | 60 minutes |
| **Room/Location** | GitHub |
| **Review Status** | Open |
| **Review name** | SWA\_template.doc |
| **Method** | DC |
| **Release** |  |
| **Responsible** | Fernando Becerra |
| **Project** | Door Control Module |
| **Reason of Review** | Initial Review |

## Comment List

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **No.** | **Reference** | **Comments / Actions** | **Classification (E)rror/Risk / (R)emark** | **Responsible person/Planned date for completion** | **Completion(Name/Date)** |
| 1 | Document Section 5.6 | Blue comment present | R |  |  |
| 2 | Document section 8 | Blue comment present |  |  |  |
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| 4 |  |  |  |  |  |
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| 6 |  |  |  |  |  |
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## Check List

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| --- | --- | --- | --- | --- | --- |
| No | Description | OK / NOK / NR | Comment | Responsible person /  Planned date for completion | Status |
| 1 | Does the design comply to the SW Requirements? | OK |  |  |  |
| 2 | Are all requirements allocated to Architectural elements? | OK |  |  |  |
| 3 | Is the system context (environment) of the software architecture described? | OK |  |  |  |
| 4 | Is a global overview of all SW function blocks (Functional Architecture) and SW components (Physical Architecture) provided, giving the reader a good understanding of the overall dependencies? | OK |  |  |  |
| 5 | Are the functions identified from SW requirements and reviewed there are no missing functions? | OK |  |  |  |
| 6 | Are the functional interfaces documented? | OK |  |  |  |
| 7 | Is each SW function mapped to one or more SW components? I.e. is there a mapping from functional to physical software architecture? | OK |  |  |  |
| 8 | Do SW components' interfaces export only the needed data and functions (encapsulation principle)? If not, are the reasons given? | OK |  |  |  |
| 9 | Are diagrams (e.g. use case charts, sequence charts…) used were appropriate? | OK |  |  |  |
| 10 | Are the SW components identified? | OK |  |  |  |
| 11 | Are the Physical Interfaces identified with a clear usage description? | OK |  |  |  |
| 12 | Are the OS task properly defined and documented? | OK |  |  |  |
| 13 | Are the Interrupt usage described? | OK |  |  |  |
| 14 | Are there Power Modes identified and documented? | OK |  |  |  |
| 15 | Is the integration Plan properly described with the corresponding order for the software construction order? | OK |  |  |  |
| 16 | Is the functional safety information properly described on the SWA document? |  |  |  |  |